Design of LCL Filter to Mitigate Harmonics in Grid Interconnected System

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ABSTRACT

Due to increasing power demand and bad effects of conventional energy sources on the environment, renewable energy sources are being important. For maximizing the power extracted from renewable energy sources such as solar and to transfer it to utility grid, power converters are being used. For maximizing the power, DC-DC boost converter plays an important role. The power generated from solar is DC and most of electrical equipments run on AC hence an inverter is required. Due to use of inverter, harmonics are generated in system. To mitigate the harmonics an LCL filter is often used. LCL filter design method with proper control technique is described in this paper. This design procedure and control techniques can be used for small and medium scale renewable energy sources.

Keywords: DC-DC Boost Converter, Power Quality, Inverter, Harmonics, Filter, Pulse-Width Modulated (PWM) Inverters

I. INTRODUCTION

In present, due to increasing world population a widely interconnected power system network is used, as requirement of the power is increasing day by day, load demand increases. Because of increasing load demand, the generated power is not sufficient for providing load requirements efficiently and effectively therefore power quality of utility is decreasing day by day. Due to bad effects of conventional energy sources on the environment and increasing power requirement, renewable energy sources are being important. For increasing the power transmission from renewable energy i.e. solar, power converters is very important. A DC-DC converter is power electronic converter which converts voltage directly from DC to DC. The input of DC-DC converter is fluctuating and uncontrolled and output of this converter is not fluctuating, regulated and controlled. This converter has variable turns ratio which is practically equivalent to transformer which works on AC. It can be used to increase or decrease a DC signal [1], [2].

As output of solar is DC and most of the electrical equipments is worked on the AC hence inverter is used [6]. Most of the inverter are Voltage Source Inverters (VSIs), which converts the DC power into AC. A filter is need to be placed between the utility grid and VSI, for mitigating the harmonics generated by inverter. We can use a series connected inductor but the harmonic mitigation of series connected inductor is not good. Also it produces the high voltage drop and required size of inductor is very large [5]. We can use LC filter, as the structure and parameter are easy, but due to uncertainty of network impedances, filtering effect is not good.

Instead of series connected inductor or LC filter, for smoothing the output from the VSI an LCL filter is used [5],[6]. By using LCL filter higher attenuation can be achieve also we can achieve cost saving and there is reduction in size and weight of the components. In grid connected inverter, pulse width modulation (PWM) is used along with LCL filter because they lowers the amount of current distortion generated by inverter in the
utility grid [5]-[7]. We can achieve good performance in the range of power levels up to several kilowatts. As LCL filter has higher harmonic attenuation which allows the use of lower switching frequency to meet the harmonic constrains as defined by IEEE standards viz. IEEE-519 and IEEE-1547[9][10]. By the observation, for designing of LCL filter sufficient information is not available. For effective result of filter we have to design appropriate mathematical model for it. The objective of this paper is do modelling and analyses of three phase LCL filter connected to inverter which transfers solar energy to grid.

II. BOOST CONVERTER

DC-DC boost converter is shown in figure1, where $E_{dc}$ is output from PV panel, L is inductor, C is a capacitor and R is resistance of load. A circuit contains the inductor in series and capacitor in parallel with the load. Boost converter operation is depends on the operation of the switch. DC-DC converter output is regulated and controlled [1].

![Figure1. Schematic diagram of Boost Converter](image)

Maximum power from the PV panel can be obtained by the DC-DC boost converter. IGBT is used as a switch in boost converter. We can control the ON-OFF of switch by giving proper gate pulses. The operation of boost converter is controlled by this switch[2].

For designing of boost converter we need to assume certain parameters which can be useful for designing L, C for boost converter. Table 1 shows the parameters which are consider for designing of boost converter.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open Circuit Voltage</td>
<td>$E_{dc}$</td>
<td>205 V</td>
</tr>
<tr>
<td>Desired output Voltage</td>
<td>$E_o$</td>
<td>400 V</td>
</tr>
<tr>
<td>Load current</td>
<td>$I_o$</td>
<td>6 A</td>
</tr>
<tr>
<td>Switching frequency of IGBT</td>
<td>$f_{sw}$</td>
<td>20000 Hz</td>
</tr>
</tbody>
</table>

With the help of parameters from the Table 1, we can design the component L and C for boost converter. In the boost converter operation there is an important role of duty cycle. For controlling the switch, proper control technique has to be provide to supply the gate pulses to the converter.

Duty cycle (D) can be find by the following formula,

$$D = 1 - \frac{E_{dc}}{E_o} \times \eta$$  \hspace{1cm} (1)

Where, $\eta$ is converter efficiency i.e. 90%.

For finding the value of inductor L, value of ripple current (di) is needed.

$$di = 2 \times I_o \times \frac{E_{dc}}{E_o}$$  \hspace{1cm} (2)

As value of ripple current is known, the value of inductor can given by,

$$L = \frac{E_{dc}(\eta_0-E_{dc})}{f_{sw}+E_{dc}+di}$$  \hspace{1cm} (3)

Similarly, for finding the value of capacitor, value of ripple voltage (dv) is needed.

$$dv = \frac{1}{1-D} \times \frac{di}{2}$$  \hspace{1cm} (4)

As value of ripple voltage is known, the value of capacitor is given by,

$$C = \frac{I_o \times D}{f_{sw}+dv}$$  \hspace{1cm} (5)

Hence, from equations (1)-(5) designing of boost converter is possible. With value of the L and C the simulation is done in MATLAB and results are given below.
III. INVERTER

The power generated by the solar sources is in DC. There is need of conversion of DC power to AC, as most of the equipments connected to utility runs on AC power. The conversion of DC-AC is done by DC-AC converter which is usually known as inverter[3]. For operating of these converters, proper gate pulse is required. Gate pulse can be generated by various means as there is lots of literature available. For generating gate pulses proper control algorithm is required.

We need to convert three phase to single phase as control of three phase is very complicated process. The conversion of three phase to single phase can be achieved by Clarke’s transformation [3]-[4]. Reference waves can be generated by using proper control algorithm and we can provide pulses using PWM technique. The equations for conversion of three phase to single phase are given below,

\[
\begin{bmatrix}
V_d \\
V_q
\end{bmatrix} = \sqrt{2/3} \begin{bmatrix}
1/2 & -1/2 & -1/2 \\
\sqrt{3}/2 & \sqrt{3}/2 & \sqrt{3}/2 \\
\end{bmatrix} \begin{bmatrix}
V_a \\
V_b \\
V_c
\end{bmatrix}
\]

(6)

When the error is find out from actual and reference value, conversion of single phase to three phase signal is required. For this conversion inverse Clarke transform is used.

\[
\begin{bmatrix}
V_a \\
V_b \\
V_c
\end{bmatrix} = \sqrt{2/3} \begin{bmatrix}
1/2 & \sqrt{3}/2 & -1/2 \\
0 & 2/\sqrt{3} & 2/\sqrt{3} \\
-1/2 & -1/2 & -1/2 \\
\end{bmatrix} \begin{bmatrix}
V_d \\
V_q
\end{bmatrix}
\]

(7)

Hence, from the above equation three phase control signal is generate. By using these three phase controlled signal as a reference wave and comparing it to carrier wave, gate pulses can be generated. By providing these generated pulses to inverter, conversion of DC-AC occurs.

IV. LCL FILTER

While injecting power in to the grid, concern about power quality is important. As the conversion of AC-DC is done by means of power converter viz. inverter, due to switching it contains harmonics. Hence modeling and designing of filter is important for mitigation of harmonics. In this paper 3rd order LCL filter is designed, which is connected in between inverter output and load[6].

A. Pre-Phase Equivalent Modeling

![Figure 3. Pre-phase model of LCL filter](image)

Figure 3 shows the schematic model of LCL filter, Table 2 shows the parameter used in figure 3.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
</tr>
</thead>
<tbody>
<tr>
<td>L_i</td>
<td>Inverter side inductor</td>
</tr>
<tr>
<td>L_g</td>
<td>Grid side inductor</td>
</tr>
<tr>
<td>C_f</td>
<td>Capacitor</td>
</tr>
<tr>
<td>R_f</td>
<td>Damping resistor</td>
</tr>
<tr>
<td>V_i</td>
<td>Input voltage</td>
</tr>
<tr>
<td>V_g</td>
<td>Output voltage</td>
</tr>
<tr>
<td>i_i</td>
<td>Inverter output current</td>
</tr>
<tr>
<td>i_g</td>
<td>Grid current</td>
</tr>
<tr>
<td>i_c</td>
<td>Capacitor current</td>
</tr>
</tbody>
</table>

V. FILTER DESIGN PROCEDURE

While designing an LCL filter, many characteristics must be considered, such as filter size, switching ripple attenuation and current ripple. The capacitor interacting with grid may cause resonance due to reactive power requirement. Therefore by connecting resistor in series
with capacitor, active or passive damping must be added [6].

For the calculation of \( L_i, L_g, C_f \) and \( R_f \) following parameters must be considered,

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input DC voltage</td>
<td>( V_{dc} )</td>
<td>400V</td>
</tr>
<tr>
<td>PWM carrier frequency</td>
<td>( f_{sw} )</td>
<td>15kHz</td>
</tr>
<tr>
<td>Nominal power</td>
<td>( P_n )</td>
<td>5kW</td>
</tr>
<tr>
<td>Attenuation factor</td>
<td>( k_a )</td>
<td>20%</td>
</tr>
<tr>
<td>Line-to-line RMS voltage</td>
<td>( V_{rms} )</td>
<td>120( \sqrt{3} )</td>
</tr>
</tbody>
</table>

VI. FREQUENCY RESPONSE OF LCL FILTER

Transfer function (T.F.) of LCL filter is \( H_{LCL} = \frac{i_g}{V_1} \), assume grid voltage as ideal voltage source, which can damp all harmonic frequencies. If \( V_g = 0 \), i.e. condition for current controlled inverters, the T.F. of LCL filter with neglecting damping is,

\[
H_{LCL}(s) = \frac{1}{L_iC_f L_g^3 + (L_i + L_f)^s} \tag{12}
\]

and T.F. with damping resistance is,

\[
H_{dLCL}(s) = \frac{R_f C_f s + 1}{L_iC_f L_g^3 + C_f (L_i + L_g) R_f s^2 + (L_g + L_f) s} \tag{13}
\]

Figure 5 shows the bode plot of LCL filter With damping and without damping. Capacitor with series resistance eliminates the gain spike. Also helps to smooth the overall response.
VII. SIMULATION RESULTS

Figure 6 shows the MATLAB/Simulink model of grid connected LCL filter. Table 4 shows the tested system parameters. With the help of these parameters system is analyzed. The output of system is analyzed for total harmonic distortion (THD) with and without filter. THD analysis is observed by the FFT tool available in MATLAB.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Symbol</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Inverter side inductor</td>
<td>$L_i$</td>
<td>2.33mH</td>
</tr>
<tr>
<td>Grid side inductor</td>
<td>$L_g$</td>
<td>0.045mH</td>
</tr>
<tr>
<td>Filter Capacitor</td>
<td>$C_f$</td>
<td>15$\mu$F</td>
</tr>
<tr>
<td>Damping resistor</td>
<td>$R_f$</td>
<td>0.55$\Omega$</td>
</tr>
</tbody>
</table>

First the THD analysis of the system is done without filter. The total harmonic distortion (THD) analysis of the system without filter is found to be 64.55%. Figure 7-A and 7-B shows the waveform of voltage and current before filter and THD analysis is shown in figure 8.

As the harmonics of the system is above the prescribed limit given by IEEE standards viz. IEEE-519 and IEEE5147 [9][10]. So we need to connect the LCL filter in series with system to mitigate harmonics. After connecting the filter, THD analysis of the system is found to be 0.19%, which is in prescribed limit. Figure 9-A and 9-B shows the waveform of voltage and current after filter and THD analysis is shown in figure 10.
VIII. CONCLUSION

This paper proposes the systematic design module of boost converter to increase the energy of solar. Also it proposes the LCL filter design model to connect the DC power generated from solar to grid with inverter and proper control technique is described. The LCL filter mitigates the harmonics present in the system. THD analysis of the system shows that the simulation model reduces the THD within prescribed limit. This design procedure and control techniques can be used for small and medium scale renewable energy sources.

IX. REFERENCES

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